

11/17/97	2 Cpl
250	Subclass
Class	ISSUE CLASSIFICATION
SEARCHED	

UTILITY SERIAL NUMBER	PATENT DATE SEP 14 1999	PATENT NUMBER 5952846
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SERIAL NUMBER	FILING DATE	CLASS	SUBCLASS	GROUP ART UNIT	EXAMINER
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APPLICANTS
None

10.

yes United Kingdom 96269725 Dec. 27, 1996

10.

Foreign priority claimed 35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	AS FILED	STATE OR COUNTRY	SHEETS DRWGS.	TOTAL CLAIMS	INDEP. CLAIMS	FILING FEE RECEIVED	ATTORNEY'S DOCKET NO.
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Verified and Acknowledged
Examiner's Initials

ADDRESS

A Low Temperature Re-Bump-Bonding Semiconductor Imaging Device

TITLE

U.S. DEPT. OF COMM / PAT. & TM PTO-436L (Rev.12-94)

PARTS OF APPLICATION
FILED SEPARATELY

4/16/97
Applications Examiner

NOTICE OF ALLOWANCE MAILED

24-9-99

Assistant Examiner

CLAIMS ALLOWED

Total Claims	Print Claim
30	1

ISSUE FEE

Amount Due *100\$ 00* Date Paid *10/10/96*

Jo.
Que T. Le
Primary Examiner

DRAWING

Sheets-Drwg.	Figs. Drwg.	Print Fig.
3	6	2

Label
Area

ISSUE
BATCH
NUMBER

R57

PREPARED FOR ISSUE

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Form PTO-436A
(Rev. 8/82)

SCAN 8 C6
QC MC
Formal Drawings (This) SCL

(FACE)

ISSUE FEE IN FILE